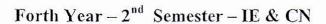
University of Technology

Computer Engineering Department

Academic Year 2016 - 2017



Embedded Systems



2 Units

2 Hours/Week

Introduction to Embedded Systems I Microcontroller Features of 8051 8051 Hardware Memory organization in 8051 I/O pins, ports, and circuits Timers and Counters Serial port Interrupt structure	2
Features of 8051	
8051 Hardware Memory organization in 8051 I/O pins, ports, and circuits Timers and Counters	
Memory organization in 8051 I/O pins, ports, and circuits Timers and Counters	
I/O pins, ports, and circuits Timers and Counters	
Timers and Counters	
Timers and Counters	8
Serial port	
Interrupt structure	
Clock and Oscillator	
Power saving options	
ruction Set.	-
Addressing modes	
Data moving instructions	- 3
	4
Assembly Language Programming	
Counter/Timer Programming	8
Interrupt Programming	
CHILLICEL	Clock and Oscillator Power saving options uction Set. Addressing modes

Text books:-

DAC Interfacing ADC Interfacing

Stepper motor Interfacing

code

- 1- A. P. Godse and D. A. Godse "Microprocessor and Microcontroller" Third Edition, 2009
- 2- Muhammed Ali Mazidi, Janice Gillispie Mazidi and Rolin D. McKinlay, "The 8051 Microcontroller and Embedded Systems", Pearson Education, Second edition, 2007.

References:-

- 1- Kenneth J. Ayala "The 8051 Microcontroller; Architecture, Programing, and Applications" 1991.
- 2- I. Scott MacKenzie "The 8051 Microcontroller", Second Edition, 1995.

Display interfacing (LED, 7-segments, dot matrix, and LCD)